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## APPLICANTS

Hua-Chang Chi, Tao-Yuan Hsien, TAIWAN;

\*\* CONTINUING DATA *None*\*\* FOREIGN APPLICATIONS *None*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* SMALL ENTITY \*\*

\*\* 08/09/2002

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	TAIWAN	7	9	2
Verified and Acknowledged	Examiner's Signature	Initials			

## ADDRESS

027765  
 NORTH AMERICA INTELLECTUAL PROPERTY CORPORATION  
 P.O. BOX 506  
 MERRIFIELD, VA  
 22116

## TITLE

Microprocessor system architecture to correct built-in ROM code

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